

Dkt. No.: MST-012

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SN: 10/003,602

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventors: Wingyu Leung and Fu-Chieh Hsu

Assignee: Monolithic System Technology, Inc.

Title: Error Correcting Memory and Method of Operating  
Same

Serial No: 10/003,602

Filing Date: 11/14/01

Examiner: Christine Tu

Art Unit: 2133

Attorney Docket No: MST-012

Date: November 3, 2005

VIA FACSIMILE

COMMISSIONER FOR PATENTS

P.O. Box 1450

Alexandria, VA 22313-1450

AMENDMENT IN RESPONSE TO FINAL OFFICE ACTION

Sir:

Applicants submit the following remarks and amendments in response to the Office Action dated May 31, 2005. A petition for a 3-month extension, extending the period for response to November 30, 2005, accompanies the present response.